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ABSTRACT OF THE DISCLOSURE

LOGICAL PARTITIONING VIA HYPERVERISOR MEDIATED ADDRESS TRANSLATION

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A method, system, and apparatus for mediating address translation in a logically partitioned data processing system is provided. In one embodiment, a firmware component receives from an operating system within a logical partition a request to access a physical resource. The firmware component, responsive to a determination that the physical resource has been allocated to the logical partition, modifies an address translation table, if necessary, to allow access to the physical resource by the operating system. The operating system is prevented from directly modifying the address translation table, thus preventing potential interference between operating systems within the logically partitioned data processing system.

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